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PATENT NUMBER

U.S. UTILITY Patent Application

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, O.I.P.E.	PATENT DATE			
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APPLICATION NO. CONT/PRIOR CLASS SUBCLASS ART UNIT **EXAMINER** 09/676197 2878 438 VONG

William Roche David Wu Massud Aminpur Scott Luning

ABANDONEL

 $\stackrel{\textstyle \bullet}{\text{Method}}$  of forming a gate electrode on a semiconductor device and a device incorporating same

ISSUING CLASSIFICATION								
ORIGIN	AL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
438	595	1438	197	585	669	659	7/4	
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TERMINAL	DRAWINGS	CLAIMS ALLOWED		
└─ DISCLAIMER	Sheets Drwg. Figs. Drwg. Print Fig.	Total Claims	Print Claim for O.G.	
	43/ 56/5	20	/	
☐ The term of this patent		NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant Examiner) //27/ai	12 - 3	3-02	
The term of this patent shall not extend beyond the expiration date	AMIR ZARABIAN	//		
of U.S Patent. No.	SUPERVISORY PATENT EXAMINER	ISSUE FEE		
	TECHNOLOGY CENTER 2800	Amount Due \$ 1280,00	Date Paid	
	(Primary Examiner) (Date)	7120010	1 da w	
The terminalmonths of this patent have been disclaimed.	Apul Down Bylo (Date)	ISSUE BAT	CH NUMBER	
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